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Please refer to UG585, Zynq-7000 All Programmable SoC Technical Reference Manual for more details. 3. Security block is shared by the Processing System and the Programmable Logic.

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For I/O operation, refer to the 7 Series FPGAs SelectIO Resources User Guide (UG471) or the Zynq-7000 SoC Technical Reference Manual (UG585). 5. The maximum limit applies to DC signals. For maximum undershoot and overshoot AC specifications, see Table 4 and Table 5. 6. See Table 12 for TMDS_33 specifications.

Zynq-7000 SoC (Z-7030, Z-7035, Z-7045, and Z-7100): DC and

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