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CMOS VLSI DESIGN Page 7 PROCESS SELECTION It is not necessary to know all process details to do CMOS integrated circuit design. However the process determines important circuit parameters such as supply voltage and maximum frequency of operation. It also determines if devices other than PMOS and NMOS transistors can be realized such as poly-to-poly

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Introduction to VLSI CMOS Circuits Design 1 Carlos Silva Cardenas Catholic University of PeruÂ´ Takeo Yoshida University of the Ryukyus Alberto Palacios Pawlovsky Toin University of Yokohama August 18, 2006
1Work supported by a grant of the Ministry of Education and Science of Japan and the Toin University of Yokohama.

Introduction to VLSI CMOS Circuits Design 1 - TOIN

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Logical Effort CMOS VLSI Design Slide 4 Example ! Ben Bitdiddle is the memory designer for the Motorola 68W86, an embedded automotive processor.

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Course Objectives and Outcomes: This course covers many aspects of the design of integrated analog-digital interface electronics in CMOS and BiCMOS VLSI technology. Emphasis is placed on practical design aspects. Topics include transistor-level design consideration as well as block-level design. Representative system applications are also included.

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